ITC2015 Call For Papers

October 6-8, 2015
Test Week™: October 4-9, 2015

International Test Conference is the world’s premier venue dedicated to the electronic test of devices, boards and systems—covering the complete cycle from design verification, design- for-test, design-for-manufacturing, silicon debug, manufacturing test, system test, diagnosis, reliability and failure analysis, and back to process and design improvement. At ITC, design, test, and yield professionals can confront the challenges the industry faces, and learn how these challenges are being addressed by the combined efforts of academia, design tools and equipment suppliers, designers, and test engineers.

ITC, the cornerstone of the Test Week™ event, offers a wide variety of technical activities targeted at test and design theoreticians and practitioners, including: formal paper sessions, tutorials, panel sessions, case studies, a lecture series, commercial exhibits and presentations, and a host of ancillary professional meetings.

Authors are invited to submit original, unpublished papers describing recent work in the field of test and design. In addition, authors are invited to submit practical, industry best practices to be included in application/lecture series sessions. Submissions simultaneously under review or accepted by another conference, symposium or journal, will be summarily rejected.

Submissions must include:
- Title of paper.
- Name, affiliation, mailing address, e-mail address, phone and fax number of each author.
- Designation of the presenter(s). ITC will communicate with the presenter.
- One or two topic(s) from the topic list, or a description of your topic.
- An electronic copy of a complete paper of 6-8 pages (maximum of 10), or an extended summary of from three to four pages. Smaller submissions are rarely accepted.
- An abstract of 35 words or less.

ITC maintains a competitive selection process for papers. Submissions must clearly describe the status of the reported work, its significance and highlights. Supporting data, results (priority is given to papers with results from real circuits) and conclusions, and references to prior work must also be included. ITC does not accept submissions that do not meet all specified criteria.

**Paper submission deadline:** February 22, 2015  (Submission deadline is firm.)
**Author notification:** May 22, 2015
**Final manuscript due:** July 17, 2015

Authors are also invited to submit a single-page poster proposal. Posters are a useful way of presenting late-breaking results, getting feedback on an innovative method, or participating without having to write a full paper. Acceptance as a poster does not preclude submission of a more complete treatment as an ITC paper in 2015. Poster proposal abstracts should be no longer than a single page. Additional information on poster abstracts and submissions can be found under the author link on the program web site.

**Poster submission deadline:** May 10, 2015
**Author notification:** July 31, 2015

Panel organizers are invited to submit panel ideas to highlight new developments and trends in test, to invite group discussion on controversies in test approaches, or to educate the audience on new standards or practices. Submissions must describe the area and possible positions of different panelists. The deadline for panel proposal submissions is: February 4, 2013.

Test Week tutorial and workshop proposals are also welcomed. Deadlines and other information about proposals can be obtained from TTTC at: http://tab.computer.org/tttc

For detailed information about the submission process, requirements and deadlines, the selection process and any other questions regarding the program itself or contact information, please consult the ITC web site at http://www.itctestweek.org.